



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Group Art Unit: 2823

Examiner: Julio J. Maldonado

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**For: A Low Dielectric Constant
Film Produced From Silicon
Compounds Comprising
Silicon-Carbon Bond**

MAIL STOP AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING
37 CFR 1.8

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3/21/05 Kent Z. Zuh
Date Signature

Dear Sir:

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

The Applicants, and the Attorney who signs below on the basis of the information supplied by the inventor and the information in his file, submit herewith patents, publications, or other information of which they are aware, which may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR § 1.56.

While the information submitted in this Supplemental Information Disclosure Statement may be material pursuant to 37 CFR § 1.56, it is not intended to constitute an admission that any patent, publication, or other information referred to therein is prior art for this invention unless specifically designated as such.

In accordance with 37 CFR § 1.97, this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or that no other possibly material information as defined under 37 CFR § 1.56(a) exists.

The patents and/or publications submitted herewith are set forth on the attached Form PTO-1449.

The Commissioner is hereby authorized to charge the sum of \$180.00 due under 37 CFR § 1.17(p) pursuant to § 1.97, and any other fee necessary to make this submission timely, to the Deposit Account No. 20-0782/AMAT/2592.C7/KMT.

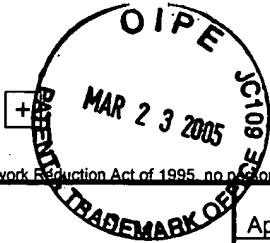
Respectfully submitted,



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PTO/SB/08a (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1

of 2

Application Number	10/756,122
Filing Date	JANUARY 13, 2004
First Named Inventor	YAU, ET AL.
Group Art Unit	2823
Examiner Name	JULIO J. MALDONADO
Attorney Docket Number	AMAT/2592.C7/DSM/LOW K/JW
Submission Date	3/21, 2005

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	A1	US-5,000,113	03/19/1991	WANG, ET AL.	
	A2				
	A3				
	A4				
	A5				
	A6				
	A7				
	A8				
	A9				
	A10				
	A11				
	A12				
	A13				
	A14				
	A15				
	A16				
	A17				
	A18				

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
	B1	EP 1 123 991 A2	08/16/2001	MICHAEL A. TODD		
	B2	EP 0 960 958 A2	12/01/1999	LOBODA, ET AL.		
	B3	WO 94/01885 A1	01/20/1994	DOBSON, ET AL.		
	B4					
	B5					

Examiner

Date Considered

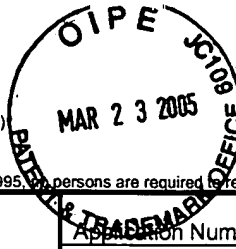
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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2

of 2

Application Number	10/756,122
Filing Date	JANUARY 13, 2004
First Named Inventor	YAU, ET AL.
Group Art Unit	2823
Examiner Name	JULIO J. MALDONADO
Attorney Docket Number	AMAT/2592.C7/DSM/LOW K/JW
Submission Date	3/21, 2005

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
	C1	BAKLI, ET AL., "Materials and Processing for .025 µm Multilevel Interconnect," Microelectronic Engineering 33 (1997) pp. 175-188.	
	C2	BHAN, ET AL., "Deposition of Stable Low K and High Deposition Rate SiF ₄ -Doped TEOS Fluorinated Silicon Dioxide (SiOF) Films," Thin Solid Films 308-309 (1997) pp. 507-511.	
	C3	C.F. LIN, ET AL., "Improved Ozone-Tetraethoxysilane Oxide Reliability for Deep Submicron Inter-Metal Dielectric Applications by Deposition of a Silane-Based Oxide Underlayer," Thin Solid Films 308-309 (1997) pp. 621-626.	
	C4	European Search Report from European Patent Office for 99906897.6 (2592.EP), dated 01/10/2005.	
	C5		
	C6		
	C7		
	C8		
	C9		
	C10		
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